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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Levine et al.

Serial No.: 10/757,250

Filed: January 14, 2004

For: Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for Use in Monitoring Performance of a Computer Program

35525
PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

§ Group Art Unit: 2192
§
§ Examiner: Kendall, Chuck O.
§

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

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Attorney Docket No.: AUS920030488US1

Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on By:

Louise Fay

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

ENCLOSED HEREWITH:

- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- References AH-AJ; and
- Our return postcard.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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Serial No: 10/757,250

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Title: Method and Apparatus for Maintaining

Performance Monitoring Structures in a

Page Table for Use in Monitoring Performance of a Computer Program

Group Art Unit: 2192

Intellectual Property Law Department International Business Machines Corporation 11400 Burnet Road Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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DOCKET NUMBER AUS920030488US1

No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

In the event a fee is required in connection with the enclosed Supplemental Information Disclosure Statement, the Commissioner of Patents and Trademarks is authorized to charge Deposit Account No. 09-0447 for the necessary amount.

Respectfully submitted,

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MATION DISCLOSURE

Complete if Known				
Application Number	10/757,250			
Filing Date	01-14-2004			
First Named Inventor	Levine et al.			
Examiner Name	Kendall, Chuck O.			
Art Unit	2192			
Attorney Docket No.	AUS920030488US1			

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	FOREIGN PATENT DOCUMENTS					
Exam Initi		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

Examiner nitial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
Al	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.
	Troffmate et al., Analysis of chared memory imposes and recording t atterno ; 2000, 1222, pp. 107 10

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.